



Document Title

A71D29/A71D39 Q&A

Revision History

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1. A71D29/A71D39跟A71x29/A71x39有哪些地方不一樣？

✧ WOR/TWOR 的 function:

- A71D29/A71D39 在進 WOR/TWOR 之前，需要下 sleep mode，否則電流會很大。

TWOR

```
void TWOR_enable(void)
{
    StrobeCMD(CMD_STBY);
    RCOSC_Cal();           //RC Oscillator Calibration

    A7129_WritePageA(GIO_PAGEA, (A7129Config_PageA[GIO_PAGEA] & 0xF000) | 0x0051); //GIO1=TWOR, GIO2=WTR

    A7129_WritePageA(WOR1_PAGEA, 0x8005); //setup TWOR SL time and AC time

    A7129_WritePageA(WOR2_PAGEA, A7129Config_PageA[WOR2_PAGEA] | 0x0014); //start TWOR by WOR_AC
    //A7129_WritePageA(WOR2_PAGEA, A7129Config_PageA[WOR2_PAGEA] | 0x001C); //start TWOR by WOR_SL

    StrobeCMD(CMD_SLEEP); //entry sleep mode

    while(GIO1==0);       //User can use this square wave to wake up MCU or other purposes
}
```

WOR

```
void WOR_enable_by_sync(void)
{
    StrobeCMD(CMD_STBY);
    RCOSC_Cal();           //RC Oscillator Calibration

    A7129_WritePageA(GIO_PAGEA, (A7129Config_PageA[GIO_PAGEA] & 0xF000) | 0x0045); //GIO1=FSYNC, GIO2=WTR

    //Real WOR Active Period = (WOR_AC[5:0]+1) x 244us - X'TAL and Regulator Settling Time
    //Note : Be aware that X'tal settling time requirement includes initial tolerance,
    //        temperature drift, aging and crystal loading.
    A7129_WritePageA(WOR1_PAGEA, 0x8005); //setup WOR Sleep time and Rx time

    A7129_WritePageA(WOR2_PAGEA, A7129Config_PageA[WOR2_PAGEA] | 0x0010); //enable RC OSC & WOR by sync
    A7129_WriteReg(MODE_REG, A7129Config[MODE_REG] | 0x0200); //WORE=1 to enable WOR function

    StrobeCMD(CMD_SLEEP); //entry sleep mode

    while(GIO1==0);       //Stay in WOR mode until receiving ID code(ID detect ok)
}
```

✧ Write FIFO/Read FIFO:

- A71D29/A71D39 在 write FIFO/Read FIFO 的時候，要在 standby mode 下才能執行，否則資料會異常。