



AN-05

Application Note for Preventing Inadvertent Flash Code Programming

November 2004

Application Notice Title

For Preventing Inadvertent Flash Code Programming

Revision History

Rev.	History	Issue Date	Remark
1.0	Initial issue	November 11, 2004	Preliminary

PRELIMINARY



User must make sure that the PIN P3.5 is not at logic low during high reset pulse, to prevent any inadvertent program or erase operations within the flash ROM code.

Table1. AP180 MODE SELECTION TABLE

External Pin Mode	Reset	P3.5
Reset Mode	1	1
JTAG Mode (ICE & ISP)	0	X
Parallel Programming mode	1	0

Notes :

1. X=Either VIH or VIL..
2. “1” for logic high, and “0” for logic low.

PRELIMINARY