



# AP180 Errata

Rev 1.2

<b>Part Number :</b>	AP180	<b>Mask Revision :</b>	D
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## 1. Data Sheet Errata

Section	Description	Corrections
Internal 1KB SRAM R/W by MOVX	When the execution of writing data to internal 1K SARM, it will causes the 1K SRAM data update error if the writing address is not equal to the last reading address.	Re-design circuit <b>This bug has been fixed.</b>

## 2. JTAG ICE for internal 1K SRAM Errata

Section	Description	Corrections
JTAG ICE for internal 1K SRAM	If setting Break-point or running Single-step at instruction1 (refer to Example1) or MOVX instruction, data update error will occur.	To avoid data update error, no Break-point or Single-step operation at Instruction1 and MOVX

Example1: Correction action

MOV RAMCON,#01H ;enable IN\_1K  
Instruction0

Instruction1  
MOVX instruction ;R/W operation for IN\_1K } No Break\_Point or Single\_Step operation at  
Instruction3 instruction1 and MOVX in JTAG ICE mode  
Instruction4