

1. ICE Driver Setup

1.1 ICE Support:

It should be supported SWD interface. Recommend to use J-LINK or ULINK Pro D.

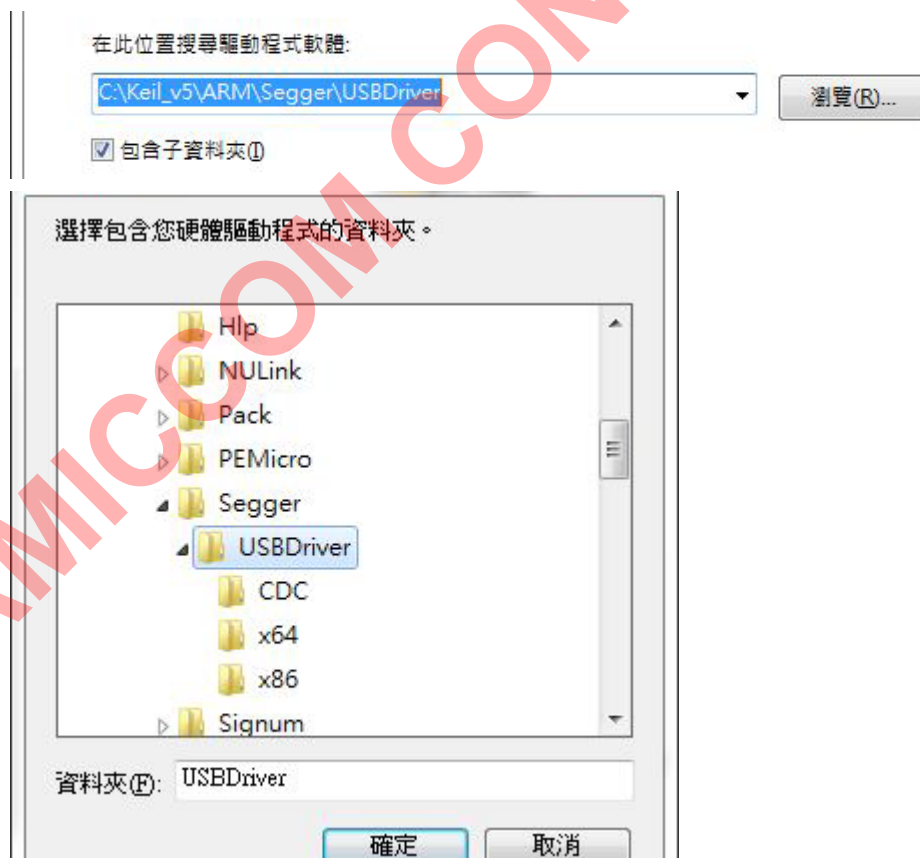
J-LINK OB will be as an example to explain the ICE installation.

1.2 Install ICE Driver

Please check whether appears following devices (J-Link driver) when ICE is connected with PC. If yes, it means that installation is correct.

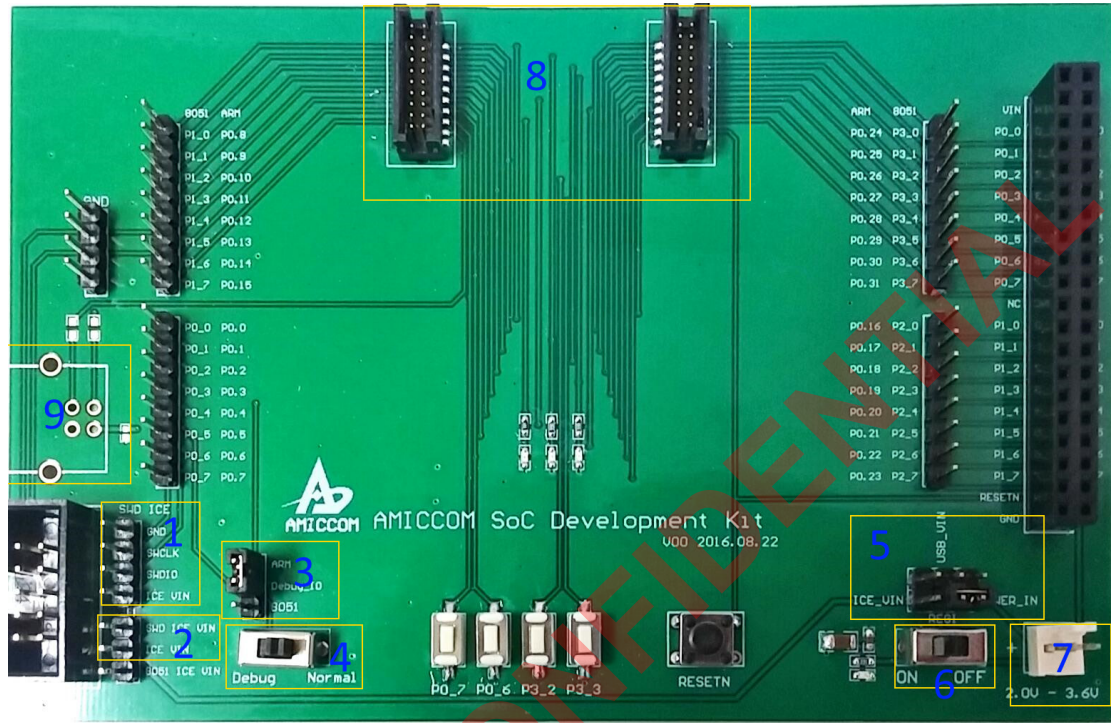


Please install Driver again if J-Link driver is not found or device appears yellow question mark. (J-Link Driver can be found in the MDK installation directory, as shown in the following path.)



2. AMICCOM SoC Development Kit

2.1 AMICCOM SoC Development Kit Hardware Description



- 1) SWD interface is connected with ICE. From top to bottom is GND, SWCLK, SWDIO, ICE VIN. Where ICE VIN is used to provide power to IC when ICE provides power output(3.3V).
- 2) ICE VIN setting, When 3.3V output from ICE to ICE VIN, to connect SWD ICE VIN and ICE VIN and connect 5) ICE_VIN and REG1, 3.3V is provided by ICE can be as input power.
- 3) Debug IO setting, ARM should be connected with Debug IO.
- 4) Debug mode switch, Debug_IO will be set to GND when turns to Debug. The corresponding IO will be set to SWCLK and SWDIO after IC reset.
- 5) Power selection :
 - By 1) ICE_VIN should be connected with REG1 when 3.3V is provided by ICE.
 - By 7) POWER_IN should be connected with REG1 when power is provided by power socket.
 - By 9) USB_VIN should be connected with REG1 when 5V is provided by USB socket.
- 6) Power switch, when turns to ON, LED will be lighted when power is provided.
- 7) Power socket, please input 2.0V~3.6V and connect 5) POWER_IN and REG1 if power provided by power socket.
- 8) Module socket.

9) USB socket.

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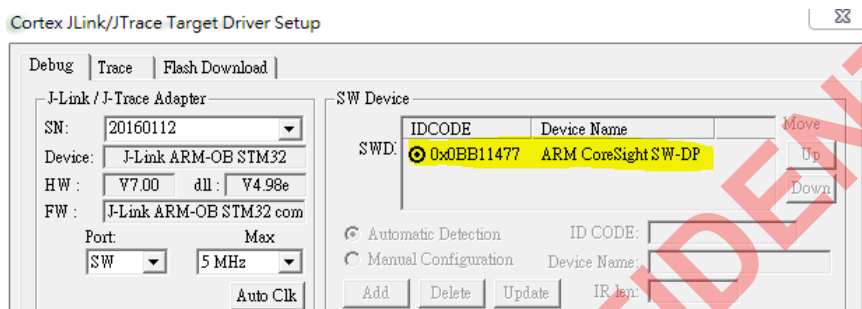
3. MDK setting

3.1 Debug page setting

To select J-LINK of Debug page in Project, as shown below.



To click Settings to enter the setting screen, and Port switches to SW. Please make sure that there is IDCODE 0x0BB11477 in SWD, as shown below.

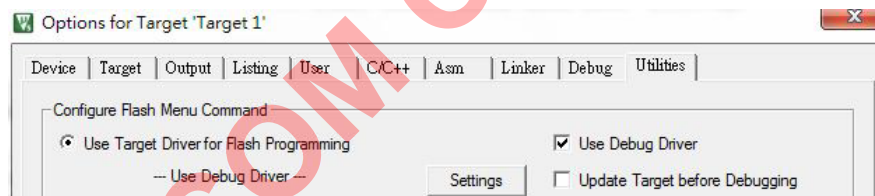


3.2 Duplicate Programming Algorithm

AMC1_F8.FLM will be duplicated to the flash folder in the MDK installation directory.

Utilities setting

To mark "Use Debug Driver" in the Utilities page.



To click Settings to enter the setting screen and click Add to add AMC1 F8 to Programming Algorithm.

RAM for Algorithm setting is set to Start: 0x20000000, Size: 0x1000.

Download Function setting is selected Erase Sector or Erase Full Chip, please also mark Program and Verify.

Cortex JLink/JTrace Target Driver Setup

Debug | Trace | Flash Download

Download Function

LOAD

☒ Erase Full Chip ☒ Program
☐ Erase Sectors ☒ Verify
☐ Do not Erase ☐ Reset and Run

RAM for Algorithm

Start: 0x20000000 Size: 0x1000

Programming Algorithm

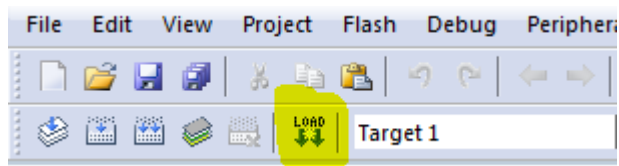
Description	Device Size	Device Type	Address Range
AMC1 P8	256k	On-chip Flash	00000000H - 0003FFFFH

Start: Size:

Add Remove

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Back to MDK project, CODE can be programmed to module by clicking LOAD.



Following similar figure will be appeared when system is programming. It means that CODE is programmed to IC correctly when Verify OK is appeared.

```
State of Pins:
TCK: 0, TDI: 0, TDO: 1, TMS: 0, TRES: 1, TRST: 1
Hardware-Breakpoints: 4
Software-Breakpoints: 8192
Watchpoints: 2
JTAG speed: 4000 kHz

Full Chip Erase Done.
Programming Done.
Verify OK.
```

To click Debug button to enter Debug mode to test IC.

